

FORM PTO-1449	Atty. Docket No.: 01-1060/L13.12-0207	Appl. No.: 10/602,570
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	First Named Inventor:	
	Mikhail I. Grinchuk et al.	
	Filing Date 06/24/03	Group Art:
	Herewith	2825

## U.S. PATENT DOCUMENTS

Examiner Initial	Document No.	Date	Name	Class	Sub Class	Filing Date If Appropriate
JSR	AA 6,367,054	4/2/02	Talwar	716	2	
JSR	AB US2001/004 4708	11/22/01	Talwar et al.	703	2	
JSR	AC US2002/002 6465	2/28/02	Rumynin et al.	708	210	
JSR	AD US2002/007 8110	6/20/02	Rumynin et al.	708	210	
JSR	AE 10/017,802	12/12/01	OPTIMIZATION OF COMPARATOR ARCHITECTURE	—	—	
JSR	AF 10/017,792	12/12/01	OPITIMZIATION OF AN ADDER BASED CIRCUIT ARCHITECTURE	—	—	
	AG					
	AH					
	AI					
	AJ					
	AK					

## FOREIGN PATENT DOCUMENTS

	Document No.	Date	Country	Class	Sub Class	Translation Yes No
	AL					
	AM					
	AN					

## OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AO	
	AP	
	AQ	

EXAMINER:

*Chambers*

DATE CONSIDERED:

2-16-05

EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.